

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
Hiroki GOKO)	Group Art Unit: 2825
Serial No. 10/813,031)	Examiner: MEMULA, Suresh
Filed: March 31, 2004)	Confirmation No. 3838
For: APPARATUS AND METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT)	Date: December 31, 2007

REQUEST FOR RECONSIDERATION

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed July 11, 2007 please consider Applicant's remarks in relation to the above identified application as follows.